





## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re patent application of

Pascal A. NSAME

Serial No.: Not Yet Assigned

Group Art Unit: Not Yet Assigned

Filing Date: Concurrently Herewith

Examiner: Unknown

For: SYSTEM-ON-A-CHIP STRUCTURE HAVING A MULTIPLE CHANNEL

BUS BRIDGE

Assistant Commissioner of Patents Washington, D.C. 20231

## INFORMATION DISCLOSURE STATEMENT

Sir:

Under the provisions of 37 CFR §1.97 through §1.99 and pursuant to applicant's duty of disclosure under 37 CFR §1.56, applicant respectfully brings the following documents listed on the attached form PTO-1449, to the attention of the Examiner in charge of the above-identified application. Copies of the listed documents are provided herewith for the convenience of the Examiner.

This citation does not constitute an admission that the references are relevant or material to the claims. They are only cited as constituting related art of which the applicant is aware.

It is respectfully requested that the listed references be considered by the Examiner and formally made of record in this application.

## BUR9-2000-0086-US1

Please charge any deficiencies in fees and credit any overpayment of fees to Attorney's Deposit Account No. 09-0456.

Respectfully submitted,

Frederick W. Gibb, III

Registration No. 37,629

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## INFORMATION DISCLOSURE CITATION

(Use several sheets if necessary)

Docket Number (Optional)	Application Number		
BUR9-2000-0071-US1	Not Yet Assigned		
Applicant(s) Pascal A. Nsame			
Filing Date	Group Art Unit		

Not Yet Assigned

				U.S. PAT	ENT DOCUMENTS			-		
*EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE		NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE		
		5,438,681	08/01/95	Mensch	, Jr.			2		
		5,455,525	10/03/95	Ho et al				C825 U.S. P. 09/708494		
		5,761,516	06/02/98	Rostoke	er et al.					
		5,842,031	11/24/98	Barker	et al.					
		6,035,364	03/07/00	Lambre	cht et al.			17-17-		
		5,838,684	11/17/98	Wicki e	t al.					
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				FOREIG	N PATENT DOCUMENTS	. <b>.!</b>	1	l		
	REF	DOCUMENT NUMBER	DATE		COUNTRY	CLASS	SUBCLASS	Translation		
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		InIN	XIDTHAY			Author, Title, Do				
		Pascal Nsame, Yvon Savaria, VIRTUALISING ON-CHIP BUS INTERFACES FOR IMPROVED EMBEDDED PROCESSOR SYSTEM PERFORMANCE, Institut National Polytechnique de Grenoble, pp. 138-143 (December 14-15, 1999).								
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EXAMINER					DATE CONSIDERED					

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP Section 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.